

Form PTO-1449 OF COMMERCE OFFICE	U.S. DEPARTMENT PATENT AND TRADEMARK INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	ATTY. DOCKET NO. 8021-54 (SS-14944-US)	SERIAL NO. Unassigned J1036 U.S. PTO 09/879434 06/12/01
		APPLICANT Duck-hyun YOO et al	
		FILING DATE Concurrently herewith	GROUP Art Unit

## U.S. PATENT DOCUMENT

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JP	US	5 2 4 5 6 3 7	9/14/93	Gersbach et al.	375	374	—
JP	US	5 8 6 4 2 5 0	1/26/99	Deng	377	165	—
JP	US	5 9 6 3 6 0 6	10/5/99	Drost et al.	375	371	—
JP	US	5 9 7 4 0 9 7	10/26/99	Julyan et al.	375	340	—
JP	US	5 5 8 7 7 0 9	12/24/96	Jeong	341	100	—
JP	US	6 0 4 4 1 2 1	3/28/00	Nolan et al.	375	354	—
JP	US	6 0 5 5 2 8 6	4/25/00	Wu et al.	375	375	—

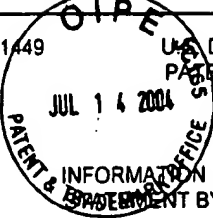
## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)


EXAMINER <i>jm</i>	DATE CONSIDERED 10/25/04
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449  INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 8021-54 (SS-14944-US)	SERIAL NO. 09/879,434
	APPLICANT Duck-Hyun Yoo		
	FILING DATE June 12, 2001	GROUP ART UNIT	

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
JP		4,189,622	2/19/80	Foshee	178		

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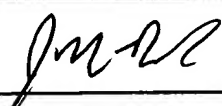
JUL 19 2004

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FOREIGN PATENT DOCUMENTS								
		PUBLICATION NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT TRANSLATION	
							YES	NO
JP		EP 0921654 A2	09/06/1999	EPO	H04L	7/033	X	
JP		EP 0921654 A3	09/06/1999	EPO	H04L	7/0333	X	
JP		EP 0738057 A2	16/10/1996	EPO	H04L	7/04		X
JP		EP 0738057 A3	16/10/1996	EPO	H04L	7/04		X

## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

		JP	Masakazu Kitazawa, <u>A Bit Synchronization Technique for PDS Optical Subscriber Loop Systems</u> , Proceedings of the Local Optical Networks, 1991.
		JP	Jun-Young Park, et al., <u>A 1.0 Gbps CMOS Oversampling Data Recovery Circuit with Fine Delay Generation Method</u> , IEICE Trans. Fundamentals, Vol. E83-A, No. 6, June 2000, pp. 1100-1105.
		JP	Chi-Kong Ken Yang, et al., <u>A 0.5 <math>\mu</math>m CMOS 4.0-Gbit/s Serial Link Transceiver with Data Recovery Using Oversampling</u> , IEEE Journal of Solid-State Circuits, Vol. 33, No. 5, May 1998, pp. 713-721.

EXAMINER 	DATE CONSIDERED 10/25/01
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\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])